

FIG. 1 (PRIOR ART)

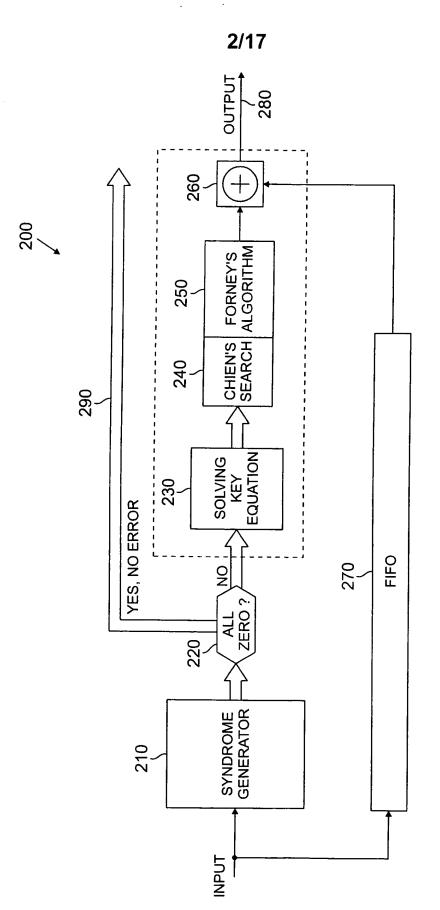
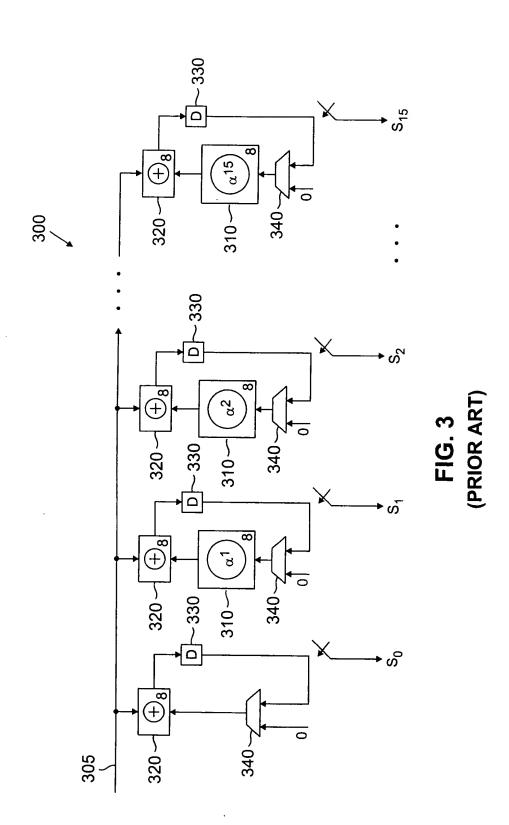


FIG. 2 (PRIOR ART)



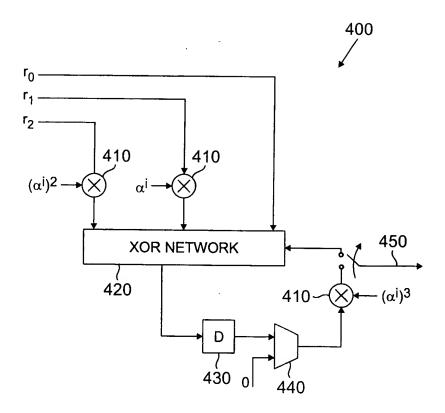
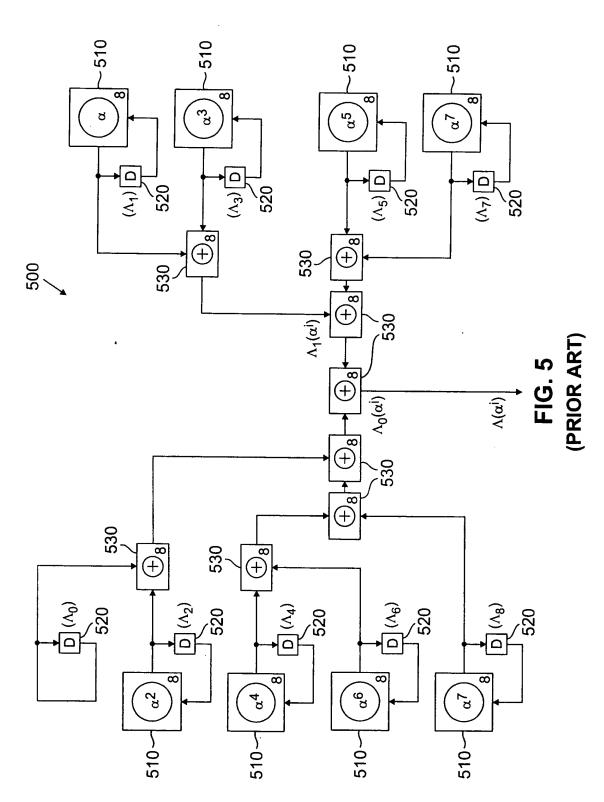
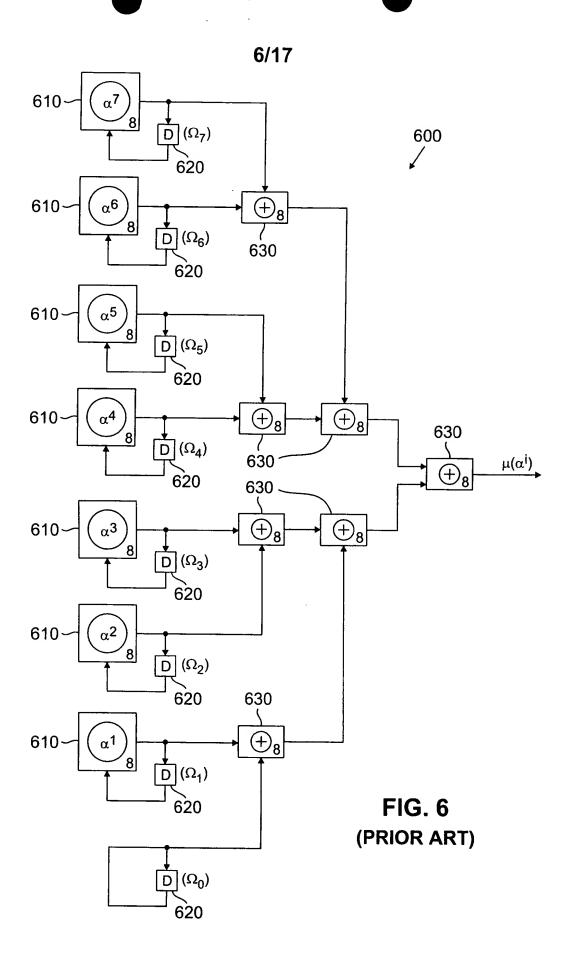


FIG. 4

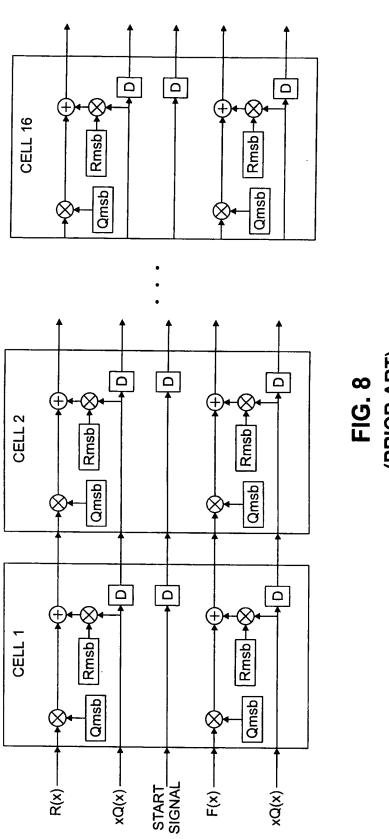




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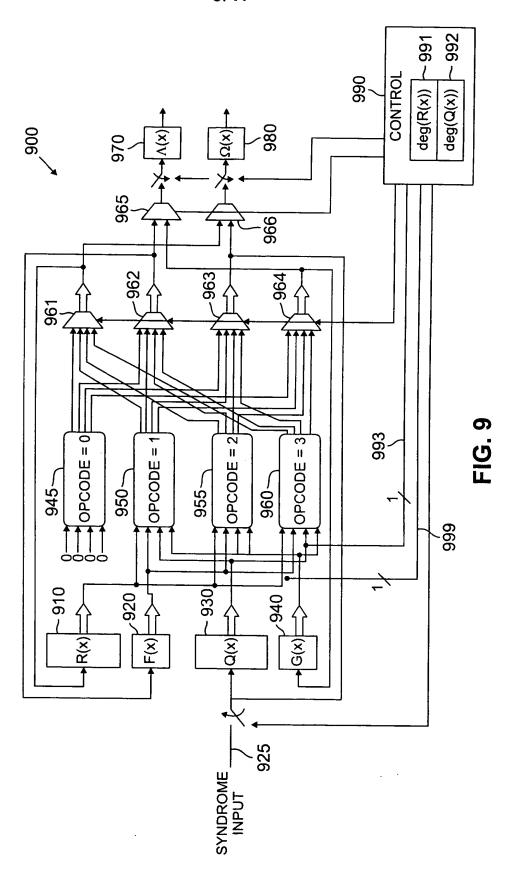
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FIG. 7 (PRIOR ART)



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FIG. 8 (PRIOR ART)



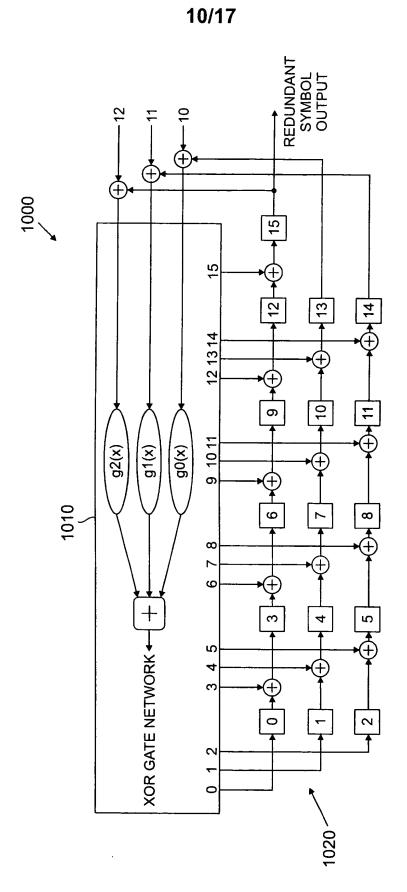


FIG. 10(a)

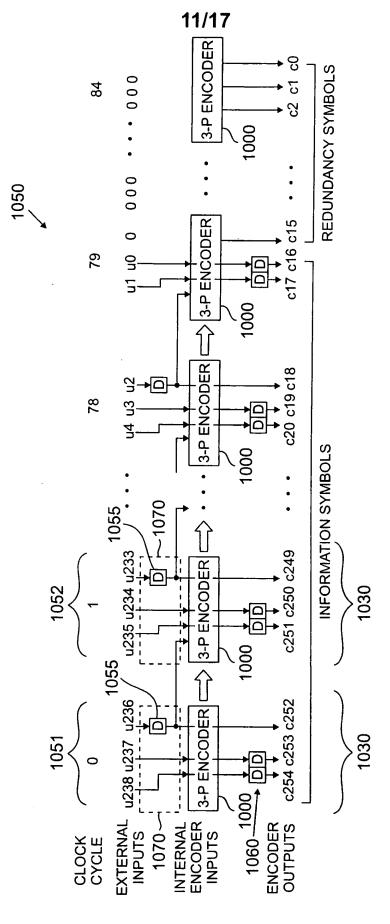
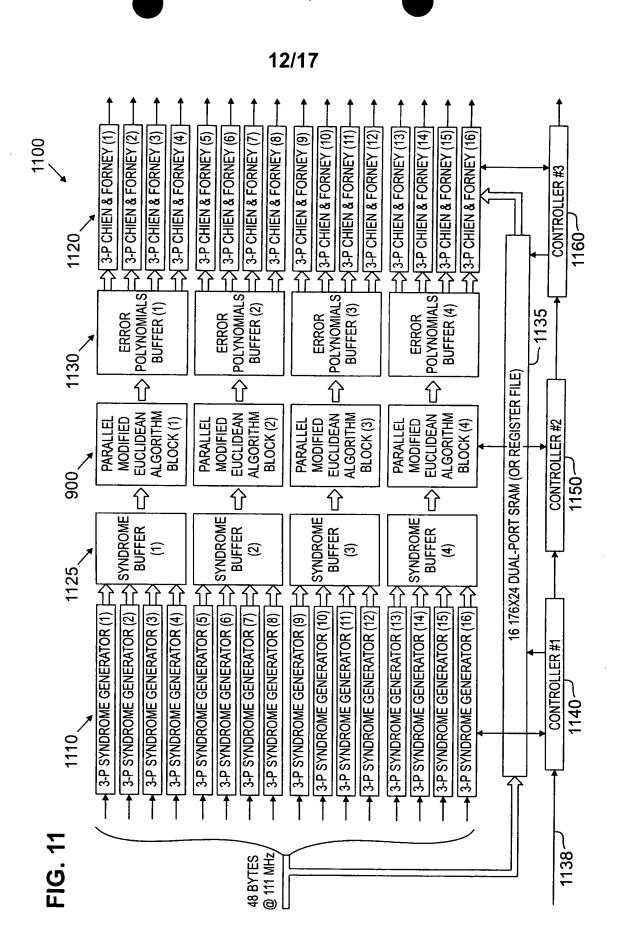


FIG. 10(b)



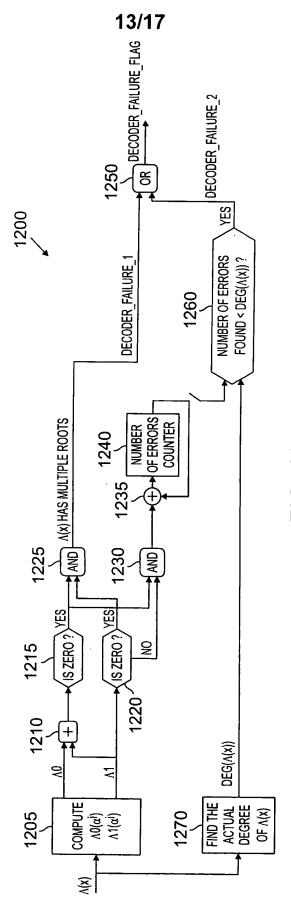


FIG. 12

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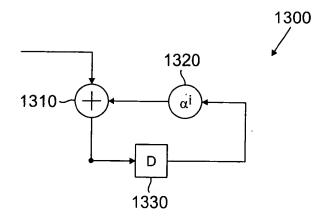


FIG. 13(a)

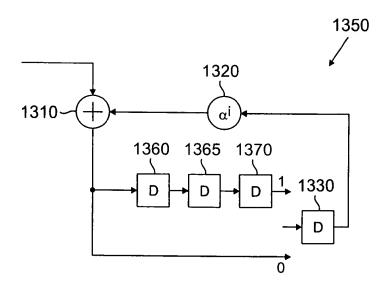


FIG. 13(b)

16 BYTES PER CYCLE @ 83 MHz

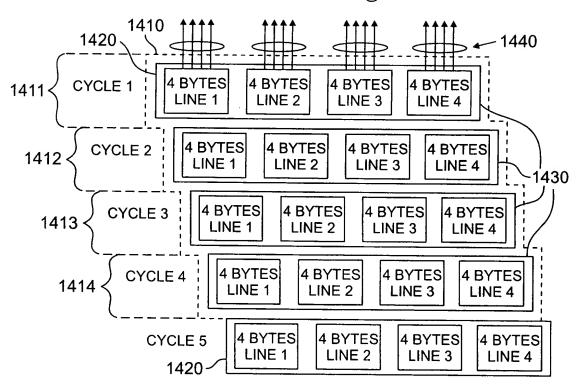


FIG. 14

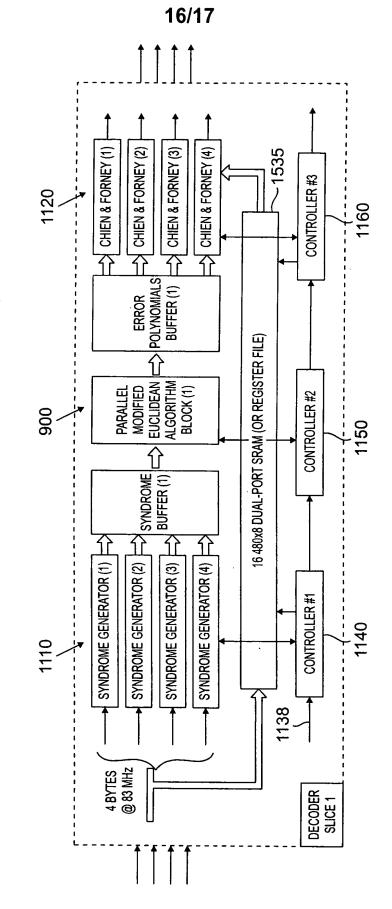


FIG. 15

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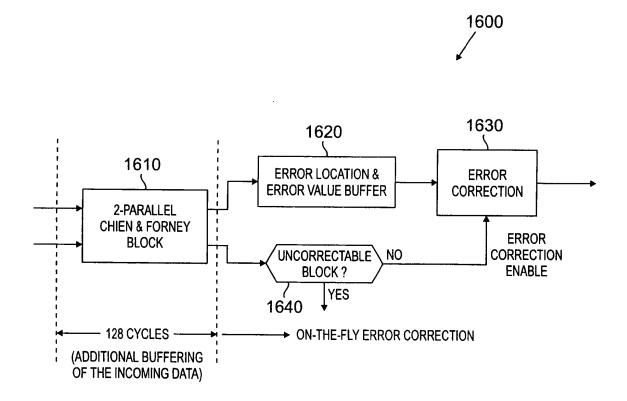


FIG. 16